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APPLICANTS

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** CONTINUING DATA ***** *NONE TDC*

** FOREIGN APPLICATIONS ***** *TDC*
 JAPAN 2003-397765 11/27/2003

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>TDC</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 3
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Verified and Acknowledged
 Examiner's Signature _____ Initials _____

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TDC 11/2/05

TITLE
 Reset circuit having synchronous and/or asynchronous modules

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